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J057 U.S. PRO

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J0675 U.S. PRO

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Oleg Zaboronski et al.

Title: LOGIC CIRCUITS FOR PERFORMING MODULAR MULTIPLICATION AND EXPONENTIATION

Attorney Docket No.: 1365.059US1

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- ☒ Return postcard.
- ☒ Utility Patent Application under 37 CFR § 1.53(b) comprising:
 - ☒ Specification (58 pgs, including claims numbered 1 through 106 and a 1 page Abstract).
 - ☒ Formal Drawing(s) (17 sheets).
 - ☒ Unsigned Combined Declaration and Power of Attorney (4 pgs).
- ☒ Applicant claims small entity status under 37 C.F.R. 1.27.

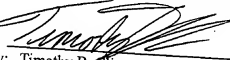
The filing fee (NOT ENCLOSED) will be calculated as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	365 - 20 =	345	x 9 =	\$3,105.00
INDEPENDENT CLAIMS	7 - 3 =	4	x 42 =	\$168.00
[X] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$140.00
BASIC FEE				\$370.00
TOTAL				\$3,783.00

THE FILING FEE WILL BE PAID UPON RECEIPT OF THE NOTICE TO FILE MISSING PARTS.

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Date of Deposit: December 20, 2001

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.